

## ABSTRACT

A pre-amplifier outputs amplified signals in response to an input optical signal, and a data detection circuit outputs a data detection signal in response to a change in a logic value of at least one of the amplified signals. A reset circuit outputs a reset signal in response to a change in a logic value of the data detection signal, and a threshold control circuit is responsive to the reset signal to remove a bias voltage which is present in at least one of the amplified signals.

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